

# The Challenges of Microelectronics for the Future Digital Society: The Roles of Thin Film Technologies and of the Higher Education

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## Abstract

The new digital society is based on Internet of Things (IoT) and related connected objects are becoming more prevalent around the world. This evolution implies innovation in many areas of technology, the heart of which is microelectronics. Connected objects involve many technological components and functions that are directly dependent of the microelectronics capabilities. If the perspectives are exciting, several challenges are appearing. The first is related to the energy consumption of all these objects, which will become enormous by 2030, and unrealistic by 2040. The second is human resources concern. The future engineers and PhD will have many obstacles to overcome. A way to face these challenges is to involve more and more new thin film technologies that must be combined with VLSI ones, and to better train students to this domain with enough know-how and with a large spectrum of knowledge suitable for multidisciplinary applications. The French national network for Higher Education in microelectronics has adopted this strategy. After presentation of the challenges, this paper deals with the innovative activities of the French network focused on thin film technologies, in order to face the challenges in a short future.

## Keywords

Microelectronics, Thin Film Technologies, Digital Society Challenges, Higher Education, Thin Film Microelectronics Devices and Systems

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## 1. Introduction

The evolution towards a digital society and the Internet of Things [1] implies the

development of a very large number of products [2] or connected objects [3]. These include multiple functions that are integrated and the main ones are sensors, actuators, analog-to-digital or digital-to-analog conversion circuits, signal processing, energy harvesting, integrated transmission and reception modules associated with transmission protocols, as well as all the usual displays, alarm and control elements [4]. These objects can have many applications, in fields as varied as the environment, health, transport, agriculture, energy, communications and security [5]. These objects will also be present in industry 4.0, corresponding to a new industrial revolution in both production and industrial tool management [6]. Usual chips may contain today several billion of elementary devices. The present systems may contain many integrated circuits and thus several hundred billion of elementary devices. Even if the consumption of electrical energy decreases at the level of elementary devices, the total consumption of the systems does not do the same. The first challenge that appears is related to the energy consumption of all these objects, which will become enormous by 2030, and unrealistic by 2040 [7]. A way to minimize this growth consists to involve thin-film technologies, particularly transistor-based technologies, and optoelectronics devices which must meet very different mission profiles depending on performance and field of application [5]. Large-area electronics and associated components are becoming increasingly important in integrated systems and especially in system-in-package (SiP), which corresponds to the stacking of circuits possibly on the same substrate but in any case in the same package [8]. It is this evolution that makes it possible to increase integration beyond Moore's law [9], which was essentially based on reducing the dimensions of elementary components during several decades. The introduction of the third dimension [10] opens a new way to significantly increase integration [8]. Therefore, it is necessary to train young graduates in these thin-film and large-area technologies and in particular thin-film transistors, both on knowledge in general and on know-how.

After a presentation of the challenges related to technology and energy consumption, attention is given to the role of thin-film technologies in this field and to the importance of higher education and practice in acquiring know-how and innovative behavior in new architectures of connected objects.

## **2. Connected Objects and Thin Film Technologies**

### **2.1. Digital Society and Microelectronics**

The development of connected objects contributes to the Internet of Things [11] which appears to be the spearhead of new societies around the world and more especially for the fourth industrial revolution, Industry 4.0. [12]. The connected objects are mainly based on electronic functions that are manufactured with microelectronic devices. They therefore require skills in all areas of microelectronics: integrated silicon electronics, analog and digital circuits, power components and circuits, high frequency and telecommunication components and circuits,



Above-ICs approach can be also involved in physical, chemical, and biological sensors with the goal to increase the area of the active functionalized zone. For example, the size of biological cells is very often much larger than the integrated elementary devices.

### 3. New Challenges for Electronic Systems

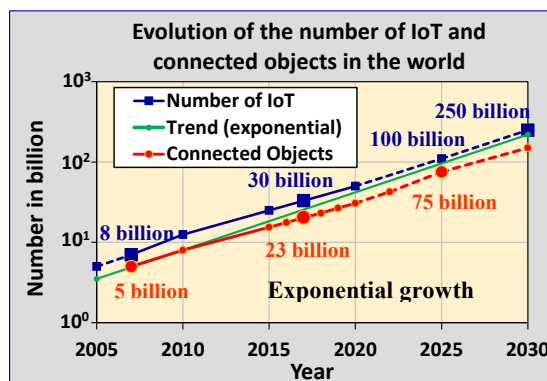
Two main types of challenges are emerging on the horizon. The first concerns the technical, technological and physical aspects, the second is related to human resources. Future engineers and technicians will have to face these technical and technological challenges.

#### 3.1. Technical and Physical Challenges

Since 2005, the development in the world of connected objects and IoT has been incredible. **Figure 2** illustrates this evolution. The growth is exponential and their number is more or less doubled every 4 years. The number of connected objects follows the same exponential law [16].

Let us notice that the number of sensors follows also an exponential growth. Indeed, their number are expected to double every two years, also [12].

As a consequence, the industry is becoming more and more competitive in the market-place. Thus, the design and manufacture of new connected objects must be carried out in an increasingly short time. Thanks to intellectual property (IP) modules, the microelectronics companies may design faster and faster new products involving assembling of several electronic functions similarly to Lego game. However, there are not optimized in terms of circuit size, clock frequency, and the number of elementary devices by function. In parallel, even if the technologists have realized a lot of improvement, due to the reduction of dimension close to atomic scale, the leakage currents are becoming relatively much higher and the losses in commutation too. As a result, the size of the electronic part of the connected objects and the associated electrical consumption are very far from the new needs in terms of consumption of natural resources and energy for both manufacturing and operation.



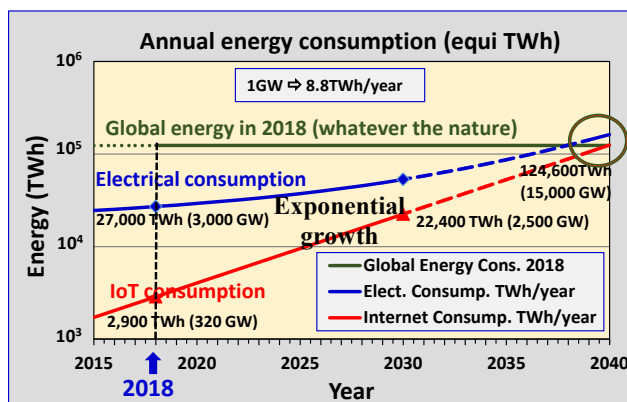
**Figure 2.** Evolution of the IoT and connected objects since 2005. In both cases, the variation is exponential. Their respective numbers double every four years.

Thus, a very important new challenge is due to the growth in energy consumption worldwide due to the exponential growth in the number of connected objects and IoT. **Figure 3** shows the evolution of overall energy consumption if the behavior of connected object manufacturers does not change in the near future. In 2018, the IoT's global consumption of electrical energy was equivalent to three times the global air traffic, or about 2900 TWh, which corresponds to a permanent electricity consumption of 325 GW. But its evolution is also exponential. The International Energy Agency forecasts a growth rate corresponding to a doubling of this consumption every four years [17].

By 2040, without modification of the behavior at the global level, IoT consumption could reach the global energy consumption of 2018, regardless of the field of use. The challenges concern all facets of the microelectronics field and more particularly all the circuits and systems that make up the connected objects. The innovation challenge must now succeed in reducing the energy consumption of basic electronic components, modifying circuit architectures to reduce the instantaneous consumption of billions of transistors, extending standby operations (or idle) to minimize the energy consumption of electronic systems, minimizing Internet use by promoting local communications (low power transmission-reception) and replacing massive silicon or semiconductor integrated circuits with thin film devices that are mostly much less energy consuming. Power devices and power electronics are also concerned in order to improve the yield of electrical conversions (AC-DC, high voltage-low voltage).

### 3.2. Human Resources Challenges

The technical and technological challenges are therefore very important and diversified. At the global level, there is a need for skills and competencies. In other words, we must train young graduates in all aspects of microelectronics, including thin-film and large-surface technologies. Training both on knowledge in general and on know-how is becoming a challenge. There is a need to train more



**Figure 3.** Evolution of the electrical energy consumption of the IoT since 2005. In both cases, the variation is exponential. The energy consumption doubles every four years. By 2040, without modification of the behavior at the global level, the energy consumption will reach the 2018 global energy consumption regardless of the field of use [17].

engineers and technicians at the production level and more doctors who can ensure research and development and innovation in all the areas mentioned above [18]. Let us notice that, even for the industrial production the fourth industrial revolution will need more and more skills for involving robots and connected manufacturing.

The problem is therefore to train students through practical training on dedicated platforms. This requires dedicated teaching platforms, accessible to students in initial training at all levels but also in lifelong learning, at a limited cost and in line with the needs of industry, teaching and research.

In France, a network has been in place since the early 1980s to create training centers dedicated primarily to education with modern platforms adapted to the constant evolution of the field. This network, entitled “CNFM” for National Coordination for Training in Microelectronics and Nanotechnologies [19] is composed of twelve interuniversity centers and two industrial partners who are in fact industrial unions [20].

These academic centers have technology and design platforms, including seven clean rooms that accommodate students from all universities and schools in a region, and a national computer-aided design service. This network is recognized by the Ministry of Higher Education. The industrial unions provide information and advices on the industrial and economic trend [21]. Strong links are thus permanently established that also encourage innovation.

Annually, this network receives 17,000 students on its eighty platforms spread over 12 centers. In order to maintain a quality of training adapted to economic needs, the network has been pursuing an innovation policy for more than ten years by issuing calls for proposals every year. Since 2012, an incentive program for innovative training as part of the French government’s excellence initiative program has reinforced this strategy. The eight-year project, entitled FINMINA [22], has been driving the innovative strategy since 2012 and has enabled new practical activities to be implemented, particularly in the field of large-area electronics and thin-film transistors. Depending on the local research and industrial environment, the centers have specialties. The network can then propose a variety of subjects best suited to the skills of the teaching teams and the equipment of the technological platforms.

In order to correspond to evolution, students can work on the realization of elementary components such as CMOS like TFTs, but also on original structures that are sorted in thin film technologies, such as polycrystalline silicon thin film transistors [23], thin film airgap transistors for sensor development [24] [25] or transistors using two-dimensional material layers [26]. These structures are issue of research activities in the close environment of the training center. Several student projects propose to go as far as the system including the packaging steps corresponding to SiP development. For these activities, all aspects can be addressed: component and circuit design, clean room manufacturing, physical and electrical characterizations, and testing of circuit, components or even systems.

## 4. Practice and Know-How on Thin Film Technologies

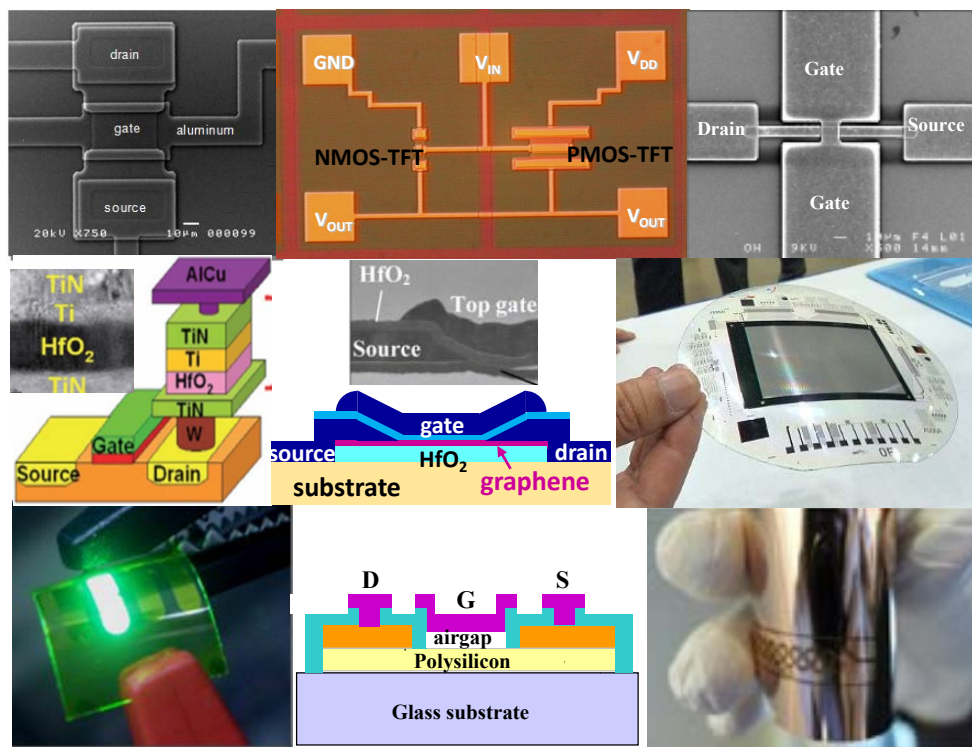
### 4.1. Importance of Practice and Know-How

Innovation, research and development require engineers, technicians or PhDs to have both skills and know-how. It is therefore necessary to adapt the training of these research, development and production staff in order to make them operational at the end of their studies and after their diplomas. As mentioned above, large-area electronics and associated components are becoming increasingly important in integrated systems and especially in system-in-package (SiP), which corresponds to the stacking of circuits possibly on the same substrate but in any case in the same package.

### 4.2. Know-How on Thin Film Technologies in the Centers

**Figure 4** shows several examples of devices carried out by students during practical training that has been implemented through the FINMINA project over the past six years. The components, mainly thin-film transistors (TFTs) in these examples [23], have been designed, technologically processed and characterized on the different platforms of the French network.

**Figure 4** shows successively, polycrystalline silicon TFT involving a silicon oxide gate insulator for CMOS circuits [27], a TFT with Hall magnetic contacts



**Figure 4.** Examples of realizations of different TFT that can be involved in electronic circuits or in sensors made by students in the frame of initial education in the French national network centers. From top left to bottom right: polycrystalline silicon TFT, CMOS-like TFT circuit, airgap TFT, OxRam thin film memory, graphene-based TFT, flexible displays, organic light emitting diode, chemical sensor, and flexible thin film circuit.

[28], a silicon nanowire TFT acting as a gas sensor [24], a TFT air gap for chemical and biological sensors [25], a graphene-based TFT [26], a flexible substrate TFT for wearable electronics.

## 5. Conclusion

Many challenges have recently arisen in the frame of the strong evolution of the society towards a digital world. The challenges are technological but also human. On the technological facet, the combination of many technologies is a way to spread the spectrum of application domains and to minimize the energy consumption. The main objective is now to train the students in an adapted environment that allows them acquiring the know-how. Only few examples were given of the effective work of the network in this paper [29]. All these projects were carried out by students as part of their initial training, at master's or engineer level. Several of them corresponding to a long manufacturing process are performed by a group of students as part of an internship. Each year, about 200 students acquire this know-how on thin film technologies in the frame of the French network [30]. We expect that they have been well prepared to develop innovative objects in the coming connected world by involving thin film technologies.

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## Conflicts of Interest

The author declares no conflicts of interest regarding the publication of this paper.

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