

# Photovoltaic Module and Submodule Level Power Electronics and Control

**G**RID-CONNECTED photovoltaic (PV) energy systems have experienced an explosive growth over the last decade, with a cumulative installed capacity surpassing the 400 GW milestone as of 2017. Among PV system configurations, distributed module-level converter architectures can lead to a higher energy yield by mitigating partial shading, persistent shading (soiling, snow, bird droppings, and fallen leaves), mismatch, and aging, through a higher maximum power point tracking (MPPT) efficiency. Also, distributed electronics might be the key for implementing diagnostic and prognostic actions at a module level. Among these configurations, microinverters (also known as ac-module inverter), which connect a single PV module to the grid, and PV power optimizers, which are dc–dc converters performing the MPPT function at a module level, have attracted the academic and industrial interest in the last decade. So much so, that both microinverters and dc–dc power optimizers are commercialized by tens of companies around the world, with a great variety of circuit topologies, which comprise combinations of one or more power stages, interleaved converters, resonant converters, topologies with and without isolation, etc.

Although compared to central, string, and multistring inverter configurations, module-level PV configurations ensure a higher level of harvested energy; they often exhibit a lower power conversion efficiency due to the additional power converter stages and due to the high-voltage step-up ratios required for the grid connection. Therefore, the design of more efficient converter stages has been and continues to be one of the main drivers of the R&D community. In addition, power density plays a fundamental role, particularly for this PV application, since the power converter is usually installed behind the PV module, in many cases between the module and the roof. More recently, reliability and fault tolerant operation have become increasingly relevant since commercial microinverters and dc–dc power optimizers aim to match, or at least shorten, the lifetime difference between the power converter and the PV module (the current warranty is typically 25 years, depending on the manufacturer). The input voltage range is a key aspect of module-level power processing systems. This is due to the operation of the bypass diodes of the substrings during partial shading, which when conducting, can produce a significant voltage drop across the PV module. This increases the voltage step-up ratio requirements up to a point in which it is not feasible for several converter topologies. The motivation for this special section has been to gather the latest

achievements on PV-module-level power electronics research to sample the current state of the art and see where it is heading.

This special section received a total of 27 papers, nine of which were accepted for publication [items 1)–9) in the Appendix]. The work in [items 1)–5) in the Appendix] deals with the topic of high step-up voltage requirements and efficiency for PV-module-level power converters. In [item 1) in the Appendix], a hybrid high step-up dc–dc converter is proposed by merging a standard boost converter with a coupled inductor and different switched-capacitor techniques, achieving a high voltage gain and efficiency, whereas lowering the voltage and current stresses of the components. In [item 2) in the Appendix], another hybrid approach is presented, this time based on a synchronous quasi-Z-source series resonant dc–dc converter with an integrated series resonant tank. The converter operates with a variable dc-link voltage that optimizes efficiency for a wide range of input voltages, for which it has been given the concept name of Optivert. The work in [item 3) in the Appendix] tackles the same issue from a different perspective by using a Cuk converter integrated with a switched-inductor technique. As a result, a high static gain with a reduced switch voltage is obtained in a nonisolated structure, resulting in less volume and losses. Another transformerless, wide input range microinverter is presented in [item 4) in the Appendix]. The converter is composed by a three-phase extended-duty-ratio boost as dc–dc stage and a doubly grounded voltage swing inverter as dc–ac stage. It is a hybrid of an interleaved boost and switched capacitor concept capable of achieving high gain while simultaneously maintaining low voltage and current stresses, whereas the common grounding feature eliminates the capacitive-coupled common-mode ground currents. In [item 5) in the Appendix], an optimized phase-shifted control strategy for a bridge modular switched capacitor converter used for energy storage in PV systems is proposed. With this control, the converter is capable of a wide regulation margin at step-down or step-up modes, capable of bidirectional power flow. In addition, the converter can operate in some regions with zero voltage switching, achieving high efficiency.

With regards to converter reliability, fault diagnostics, and fault tolerant operation, three contributions are included in this special section [items 6)–8) in the Appendix]. The work in [item 6) in the Appendix] deals with the wear-out failure of an impedance-source PV microinverter. The analysis is performed based on system-level electrothermal modeling combined with finite-element simulations and experimental measurements. The study reveals that the dc-link electrolytic capacitor is the component affecting long-term reliability. Changes in the dc-link

capacitor and in the control, including variable dc-link voltage operation, are proposed to achieve reliability improvements. In relation to fault diagnostics, the work in [item 7) in the Appendix] introduces fault detection at module level based on monitored data by the microinverter and day-ahead power forecasting. In addition, a suitable procedure has been proposed for real-time monitoring of fault conditions by comparing neighbor PV modules. Results presented in this paper showed that this approach is able to identify critical failures of PV modules, avoiding systematic errors like aging or regular shadows. On the other hand, in [item 8) in the Appendix] proposes a redundant synchronous switch in a two-stage buck/buck-boost converter to enable fault-tolerant operation during open-switch faults. Due to the way the circuit reconfigures during fault, postfault operation still retains full power capabilities of the converter. An optimized fault-tolerant control, suitable in both healthy and postfault operations, is also presented in the paper.

Finally, the work in [item 9) in the Appendix] deals with the practical implementation of communications between dc-dc power optimizers. This is achieved by the authors, by embedding the power line communication data modulated directly into the control loop of the power converter, and then transmitted through the series-connected dc power line to other power optimizer. One of the advantages is that only few additional components are required for the implementation of the method. The performance, quality, and limitations of the communications are also discussed in the paper and validated experimentally.

We hope this special section serves as a reference and update for academics, researchers, and practicing engineers in order to inspire new research and developments that can pave the way for the next generation of microinverters and dc-dc power optimizers.

GIOVANNI SPAGNUOLO, *Guest Editor*  
University of Salerno  
84084 Salerno, Italy

SAMIR KOURO, *Guest Editor*  
Universidad Tecnica Federico Santa Maria  
Valparaíso 2390123, Chile

DMITRI VINNIKOV, *Guest Editor*  
Tallinn University of Technology  
19086 Tallinn, Estonia

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## APPENDIX RELATED WORK

- 1) A. M. S. S. Andrade, L. Schuch, and M. L. da Silva Martins, "Analysis and design of high-efficiency hybrid high step-up dc-dc converter for distributed PV generation systems," *IEEE Trans. Ind. Electron.*, vol. 66, no. 5, pp. 3860–3868, May 2019.
- 2) D. Vinnikov, A. Chub, E. Liivik, R. Kosenko, and O. Korkh, "Solar optivertor—A novel hybrid approach to the photovoltaic module level power electronics," *IEEE Trans. Ind. Electron.*, vol. 66, no. 5, pp. 3869–3880, May 2019.
- 3) J. C. dos Santos de Moraes, J. L. dos Santos de Moraes, and R. Gules, "Photovoltaic AC-module based on a Cuk converter with a switched-inductor structure," *IEEE Trans. Ind. Electron.*, vol. 66, no. 5, pp. 3881–3890, May 2019.
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- 5) L. He and Y. Ding, "Optimized phase-shift control strategy of bidirectional BMSCC with continuous conversion ratio and high efficiency operation," *IEEE Trans. Ind. Electron.*, vol. 66, no. 5, pp. 3902–3913, May 2019.
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- 7) S. Leva, M. Mussetta, and E. Ogliari, "PV module fault diagnosis based on microconverters and day-ahead forecast," *IEEE Trans. Ind. Electron.*, vol. 66, no. 5, pp. 3928–3937, May 2019.
- 8) S. Siouane, S. Jovanović, and P. Poure, "Open-switch fault-tolerant operation of a two-stage buck/buck-boost converter with redundant synchronous switch for PV systems," *IEEE Trans. Ind. Electron.*, vol. 66, no. 5, pp. 3938–3947, May 2019.
- 9) Y. Zhu, J. Wu, R. Wang, Z. Lin, and X. He, "Embedding power line communication in photovoltaic optimizer by modulating data in power control loop," *IEEE Trans. Ind. Electron.*, vol. 66, no. 5, pp. 3948–3958, May 2019.



**Giovanni Spagnuolo** (M'98–SM'10–F'16) received the master's degree in electronic engineering from the University of Salerno, Fisciano, Italy, in 1993, and the Ph.D. degree in electrical engineering from the University of Napoli "Federico II," Naples, Italy, in 1997.

In 1999, he joined the University of Salerno as an Assistant Professor, then as an Associate Professor, and since May 2016, he has been a Full Professor of Electrical Engineering. Since 2018, he has been a Visiting Professor with Tampere University of Technology, Tampere, Finland. He is in the Thomson Reuters 2015 list of the Most Influential Researchers. He is the coauthor of six international patents, some of them owned by companies. He is the author of about 200 papers published in international journals and in proceedings of conferences.

Dr. Spagnuolo has been an IEEE Fellow "for contributions to control of photovoltaic systems" since January 2016. From October 2012 to October 2014, he was the Chairman of the Technical Committee on Renewable Energy Systems of the IEEE Industrial Electronics Society. He has been involved in the scientific committee of many editions of IES sponsored conferences, such as ISIE, IECON, ICIT, ELECTRIMACS, POWERENG, and ICCEP. Since 2017, he has been a Member of the IEEE European Public Policy Initiative Working Group on Energy. He is an Editor for the IEEE JOURNAL OF PHOTOVOLTAICS and an Associate Editor for the IEEE TRANSACTIONS ON INDUSTRIAL ELECTRONICS.



**Samir Kouro** (S'04–M'08–SM'17) received the M.Sc. and Ph.D. degrees in electronics engineering from the Universidad Tecnica Federico Santa Maria (UTFSM), Valparaíso, Chile, in 2004 and 2008, respectively.

In 2007, he joined the Electronics Engineering Department, UTFSM, as a Research Assistant, and has served this institution in different positions, currently as an Associate Professor. From 2009 to 2011, he was a Postdoctoral Fellow with the Department of Electrical and Computer Engineering, Ryerson University, Toronto, ON, Canada. He is a Principal Investigator of the Solar Energy Research Center and Titular Researcher of the Advanced Center of Electrical and Electronics Engineering, both centers of excellence in Chile. He has coauthored one book, seven book chapters, and more than 160 journal and conference papers. His research interests include power electronics, control, renewable energy conversion systems (photovoltaic and wind), and electromobility applications.

Dr. Kouro was a Guest Editor for five special sections in IEEE journals and one for IET. He was an Associate Editor for the *International Journal of Electrical Power and Energy Systems* of Elsevier in 2016–2017. He has been included in the 2018 Clarivate Highly Cited Researchers list. He was the recipient of the 2016 IEEE Industrial Electronics Bimal K. Bose Award for Industrial Electronics Applications in Energy Systems, the 2015 IEEE Industrial Electronics Society J. David Irwin Early Career Award, the 2012 IEEE Power Electronics Society Richard M. Bass Outstanding Young Power Electronics Engineer Award, the 2015 Second Prize Paper Award of the IEEE TRANSACTIONS ON POWER ELECTRONICS, the 2012 IEEE Industry Applications Magazine First Prize Paper Award, the 2012 IEEE TRANSACTIONS ON INDUSTRIAL ELECTRONICS Best Paper Award, and the 2008 IEEE Industrial Electronics Magazine Best Paper Award.



**Dmitri Vinnikov** (M'07–SM'11) received the Dipl.Eng., M.Sc., and Dr.Sc. Techn. degrees in electrical engineering from the Tallinn University of Technology, Tallinn, Estonia, in 1999, 2001, and 2005, respectively.

He is currently a Research Professor and the Head of the Power Electronics Group, Department of Electrical Power Engineering and Mechatronics, Tallinn University of Technology, and a Guest Researcher with the Institute of Industrial Electronics and Electrical Engineering, Riga Technical University, Riga, Latvia. He is the CTO and cofounder of Ubik Solutions LLC—an Estonian start-up company dedicated to innovative and smart power electronics for renewable energy systems. Moreover, he is one of the founders and leading researchers of ZEBE—an Estonian Center of Excellence for zero energy and resource efficient smart buildings and districts. He has authored or coauthored two books, five monographs, one book chapter, as well as more than 200 published papers on power converter design and development, and is the holder of numerous patents and utility models in this field. His research interests include applied design of power electronic converters and control systems, renewable energy conversion systems (photovoltaic and wind), impedance-source power converters, and implementation of wide bandgap power semiconductors.

D. Vinnikov is a Chair of the IES/PELS Joint Societies Chapter of the IEEE Estonia section. He has been the Guest Editor-in-Chief of a special section for the IEEE TRANSACTIONS ON POWER ELECTRONICS and the IEEE TRANSACTIONS ON INDUSTRIAL ELECTRONICS, and for the last four years he has been serving as the Associate Editor for the IEEE TRANSACTIONS ON POWER ELECTRONICS. He is leading the inverters/rectifiers subcommittee of the Power Electronics Technical Committee of the IEEE-IES and is a member of the Student and Young Professionals Activity Committee of the IEEE-IES. He was the recipient of the Best Young Scientist and Scientist of the Year awards of Tallinn University of Technology in 2010 and 2017, respectively, the Estonian National Research Award in Technical Sciences for the contribution to innovative power electronic systems for renewable energy applications in 2014, and the Doctor Honoris Causa from Chernihiv National University of Technology, Chernihiv, Ukraine, in 2016.