COMPLIANT

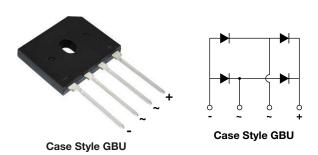
HALOGEN

FREE



### Vishay General Semiconductor

# Low V<sub>F</sub> Single-Phase Single In-Line Bridge Rectifier



#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	25 A			
$V_{RRM}$	600 V			
I <sub>FSM</sub>	350 A			
V <sub>F</sub> at I <sub>F</sub> = 12.5 A (125 °C)	0.75 V			
T <sub>J</sub> max.	175 °C			
Package	GBU			
Circuit configuration	In-line			

#### **FEATURES**

- UL recognition file number E312394
- Oxide planar chip junction

Low forward voltage drop

• Solder dip 275 °C max. 10 s, per JESD 22-B106

• Ideal for printed circuit boards

- · High surge current capability
- High case dielectric strength of 1500 V<sub>RMS</sub>
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

General purpose use in AC/DC bridge full wave rectification for switching power supply, home applications, and white-goods applications specially or telecom power supply, high efficiency desktop PC and server SMPS.

#### **MECHANICAL DATA**

Case: GBU

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and

industrial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD22-B102

M3 suffix meet JESD 201 class 1A whisker test

Polarity: as marked on body

**Mounting Torque:** 10 cm-kg (8.8 inches-lbs) max. **Recommended Torque:** 5.7 cm-kg (5 inches-lbs)

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	GBUE2560	UNIT		
Device marking code			GBUE2560			
Maximum repetitive peak reverse voltage		$V_{RRM}$	600	V		
Maximum RMS voltage		V <sub>RMS</sub>	420	V		
Maximum DC blocking voltage		$V_{DC}$	600	V		
Maximum average forward rectified output current at	T <sub>C</sub> = 140 °C	I <sub>O</sub> <sup>(1)</sup>	25	^		
	T <sub>A</sub> = 25 °C	I <sub>O</sub> <sup>(2)</sup>	4.9	A		
Non-repetitive peak forward surge current 8.3 ms single sine-wave, T <sub>J</sub> = 25 °C		I <sub>FSM</sub>	350	А		
Rating for fusing (t < 8.3 ms)		l <sup>2</sup> t	508	A <sup>2</sup> s		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C		

#### Notes

- (1) Unit case mounted on aluminum plate heatsink
- (2) Units mounted on PCB without heatsink



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Maximum instantaneous forward voltage drop	I <sub>E</sub> = 12.5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.88	0.92	V
per diode	IF = 12.5 A	T <sub>A</sub> = 125 °C	<b>v</b> F \.,	0.75	-	V
Maximum DC reverse current at rated DC	V <sub>R</sub> = 600 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.1	10	
blocking voltage per diode	v <sub>R</sub> = 000 v	T <sub>A</sub> = 125 °C	IR (−/	27	-	μΑ
Typical reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		t <sub>rr</sub>	280	-	ns
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	240	-	pF

#### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL GBUE2560			
Typical thermal registance	R <sub>0JA</sub> (1)	23	°C/W	
Typical thermal resistance	R <sub>0</sub> JC (2)	1.2	C/VV	

#### **Notes**

- (1) Without heatsink, free air
- (2) With heatsink

ORDERING INFORMATION						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
GBUE2560-M3/P	3.83	Р	20	Tube		

### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

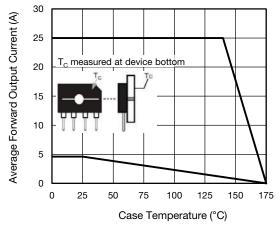


Fig. 1 - Derating Curve Output Rectified Current

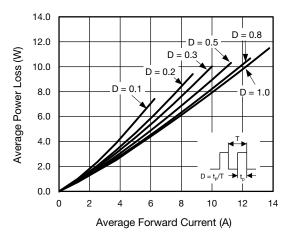


Fig. 2 - Forward Power Loss Characteristics Per Diode



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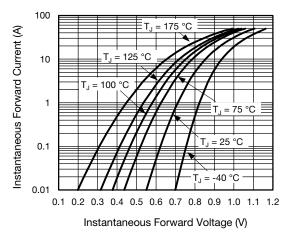


Fig. 3 - Typical Forward Characteristics Per Diode

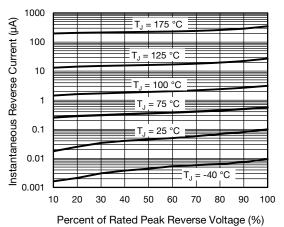


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

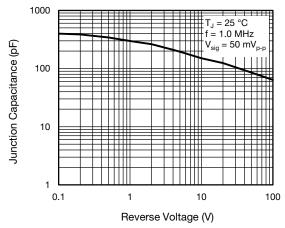


Fig. 5 - Typical Junction Capacitance Per Diode

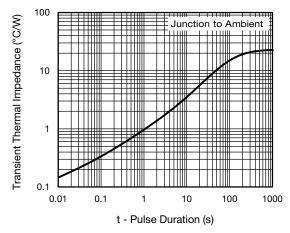


Fig. 6 - Typical Transient Thermal Impedance Per Diode

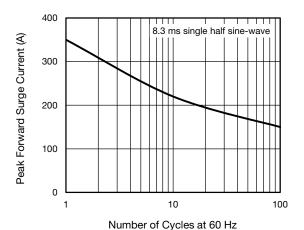
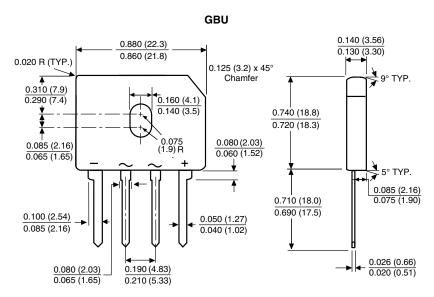


Fig. 7 - Peak Forward Surge Current

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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



Polarity shown on front side of case, positive lead by beveled corner



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